

Patent Abstracts of Japan

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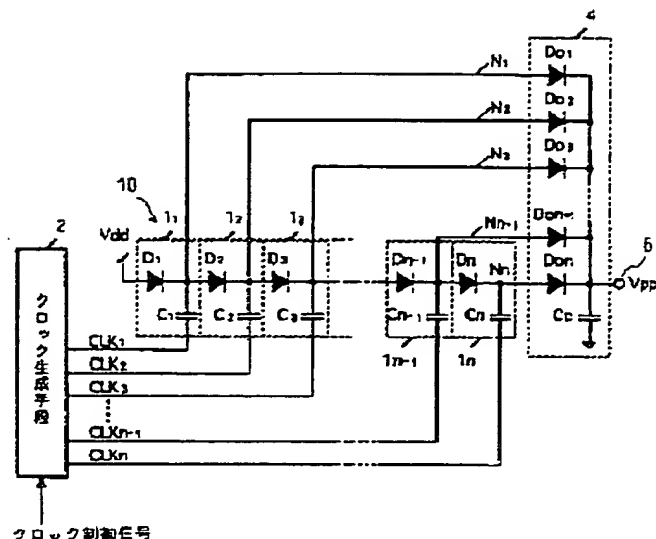
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TITLE : SEMICONDUCTOR STEP-UP CIRCUIT



ABSTRACT : PROBLEM TO BE SOLVED: To make it possible to selectively take out the output voltage of a plurality of levels and reduce power consumption by a simple circuit configuration.

SOLUTION: This step-up circuit is provided with a charge pump means 10 which, being constituted of a series combination of a plurality of pump blocks 1₁-1_n for conducting a step-up operation by charging and discharging capacitive elements C₁-C_n by means of step-up clocks CLK₁-CLK_n, generates the step-up voltage, a clock-generating means 2 for supplying the step-up clocks CLK₁-CLK_n to the pump blocks 1₁-1_n which constitute the charge pump means 10, and an output rectifying means 4 which connects at least one of the outputs of the pump blocks 1₁-1_n of the charge pump means 10 to a capacitive element C₀ via the rectifying diodes D₀₁-D_{0n}.

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